

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	0	(heat adj dissipating adj (housing or box or enclosure)) and pins and jel	USPA T	2004/01/14 14:05			0
2	BRS	L2	16	(heat adj dissipating adj (housing or box or enclosure)) and pins	USPA T	2004/01/14 14:07			0
3	BRS	L3	48	(heat adj dissipating adj (housing or box or enclosure))	USPA T	2004/01/14 14:09			0
4	BRS	L4	32	(heat adj dissipating adj (housing or box or enclosure))	EPO; JPO; DERW ENT; IBM- TDB	2004/01/14 14:11			0
5	BRS	L5	8	(heat adj dissipating adj (housing or box or enclosure))	US-P GPUB	2004/01/14 14:11			0
6	BRS	L6	8	(heat adj dissipating) adj (housing or box or enclosure)	US-P GPUB	2004/01/14 14:12			0
7	BRS	L7	697	(heat adj dissipating) and (housing or box or enclosure)	US-P GPUB	2004/01/14 14:13			0
8	BRS	L8	159	7 and pins	US-P GPUB	2004/01/14 14:12			0
9	BRS	L9	0	8 and (hybrid adj circuit)	US-P GPUB	2004/01/14 14:12			0
10	BRS	L10	1	7 and (hybrid adj circuit)	US-P GPUB	2004/01/14 14:13			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
11	BRS	L11	2743	(heat adj dissipating) and (housing or box or enclosure)	USPA T	2004/01/14 14:16			0
12	BRS	L12	1	10 and (hybrid adj circuit)	US-P GPUB	2004/01/14 14:15			0
13	BRS	L13	0	10 and jel	US-P GPUB	2004/01/14 14:15			0
14	BRS	L14	0	11 and jel	US-P GPUB	2004/01/14 14:15			0
15	BRS	L15	443	(heat adj dissipating) same cover	USPA T	2004/01/14 14:16			0
16	BRS	L16	106	15 and pins	USPA T	2004/01/14 14:27			0
17	BRS	L17	0	15 and jel	USPA T	2004/01/14 14:27			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	548	((board or substrate) and ((housing same cover same (bottom or base) same pins))	USPA T	2004/01/14 12:51			0
2	BRS	L2	82	1 and cooling	USPA T	2004/01/14 12:39			0
3	BRS	L3	466	1 not 2	USPA T	2004/01/14 12:40			0
4	BRS	L4	16	((housing same cooling same cover same (bottom or base) same pins))	USPA T	2004/01/14 12:53			0
5	BRS	L5	6	((housing same cooling same cover same (bottom or base) same pins))	EPO; JPO; DERW ENT; IBM- TDB	2004/01/14 13:35			0
6	BRS	L6	13	((housing same (cooling or heat) same cover same (bottom or base) same pins))	EPO; JPO; DERW ENT; IBM- TDB	2004/01/14 13:36			0
7	BRS	L7	82	((housing same (cooling or heat) same cover same (bottom or base) same pins))	USPA T	2004/01/14 13:36			0
8	BRS	L8	66	7 not 4	USPA T	2004/01/14 13:36			0